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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey S. Mailloux et al.

Title: MEMORY DEVICE WITH PATTERNED AND PATTERNLESS ADDRESSING

Docket No.: 303.623US2

Serial No.: 08/984,560

Filed: December 3, 1997

Due Date: November 14, 2001

Examiner: Hong Kim

Group Art Unit: 2751

Commissioner for Patents  
Washington, D.C. 20231

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Technology Center 2100

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response (4 Pages).
- ☒ A Clean Version of Specification Paragraphs (1 pg.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 30 day of October, 2001.

Name

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Signature

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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S/N 08/984,560

PATENT

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ADDRESSING

**AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111**

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The Applicant has reviewed the Office Action mailed on August 14, 2001. Detailed comments to the Office Action are found in the Remarks section below. Please amend the above-identified patent application as follows, and consider the appended remarks.

**IN THE SPECIFICATION**

Please make the paragraph substitutions indicated in the appendix entitled Clean Version of Amended Specification Paragraphs. The specific changes incorporated in the substitute paragraphs are shown in the following marked-up versions of the original paragraphs:

**Please amend paragraph 1, on page 1 as follows:**

E 1 This application is a divisional of application Serial No. 08/650,719, filed May 20, 1996, which is a Continuation-In-Part of application Serial No. 08/584,600, filed January 1, 1996, now U.S. Patent No. 5,966,724.

[ **Please amend the paragraph beginning on page 38, line 11, as follows:**

E 2 In column-based switching, switching between burst EDO and pipelined EDO modes is accomplished on successive /CAS cycles. Moreover, this type of switching may be accomplished on either read or write cycles, e.g., from a burst EDO read cycle to a pipelined EDO read cycle and [vise-versa] vice-versa, or from a burst EDO write cycle to a pipelined EDO write cycle and [vise-versa] vice-versa.